

ISSUE CLASSIFICATION	
Class	Subclass

FILED UNDER 25 U.S.C. 871

PATENT NUMBER

U.S. UTILITY Patent Application

FF	O.I.R.E.	PATENT DATE
SCANNED	940	
	O.A.	

APPLICATION NO. 09/486561	CONT/PRIOR D F	CLASS 257	SUBCLASS G-5	ART UNIT 2811	EXAMINER CARIZ
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NOBUAKI HASHIMOTO

SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURE THEREOF, CIRCUIT BOARD,
AND ELECTRONIC DEVICE

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE
Amount Due					Date Paid
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